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**TITLE:** **ENERGY RECOVERING APPARATUS AND METHOD FOR  
PLASMA DISPLAY PANEL**

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# ENERGY RECOVERING APPARATUS AND METHOD FOR PLASMA DISPLAY PANEL

## BACKGROUND OF THE INVENTION

### 1. Field of the Invention

[1] This invention relates to an energy recovering apparatus and method for a plasma display panel, and more particularly to an energy recovering apparatus and method thereof.

### 2. Background of the Related Art

[2] Recently, there has been developed various flat panel devices that are capable of reducing weight and bulk. Such flat panel display devices include a liquid crystal display (LCD), a field emission display (FED), a plasma display panel (PDP) and an electro-luminescence display (ELD), organic and inorganic EL, etc.

[3] The PDP uses a gas discharge, and allows manufacture of a large-dimensional panel. The PDP typically includes three electrodes which is preferably driven with an AC voltage.

[4] Referring to Fig. 1, a discharge cell of the conventional PDP preferably includes a first electrode 12Y and a second electrode 12Z provided on an upper substrate 10, and an address electrode 20X provided on a lower substrate 18.

[5] On the upper substrate 10 provided preferably with the first electrode 12Y and the second electrode 12Z in parallel, an upper dielectric layer 14 and a protective film 16 of preferably MgO are disposed. Wall charges generated upon plasma discharge are preferably accumulated near the upper dielectric layer 14.

[6] A lower dielectric layer 22 and barrier ribs 24 are preferably formed on the lower substrate 18 provided with the address electrode 20X. The surfaces of the lower dielectric layer 22 and the barrier ribs 24 are preferably coated with a fluorescent (e.g. phosphorous) material 26. The address electrode 20X is preferably formed in a direction crossing the first electrode 12Y and the second electrode 12Z. The barrier rib 24 is preferably formed in parallel to the address electrode 20X. Gas, preferably inactive gas, is provided in a discharge space defined between the upper and lower substrate 10 and 18 and the barrier rib 24. As can be appreciated, other layouts of the PDP structure is possible.

[7] The PDP is preferably driven based on a number of sub-fields. In each sub-field interval, a light emission having a frequency proportional to a weighting value of a video data is conducted to provide a gray scale display. Each sub-field is divided into an initialization period, an address period, a sustain period and an erasure period.

[8] The initialization period is generally a period for forming wall charges on the discharge cell. The address period is a period for generating a selective address discharge in accordance with, e.g., a logical value of the video data. The sustain period is a period for allowing a discharge cell in which the address discharge has been generated to sustain a discharge. The erasure period is a period for erasing a sustain discharge generated in the sustain period.

[9] The address discharge and the sustain discharge of the PDP driven in the above manner requires a high voltage, e.g., more than hundreds of volts. Accordingly, an energy recovering apparatus is used for the purpose of minimizing a driving power required for the address discharge and the sustain discharge. The energy recovering

apparatus recovers a voltage between the first electrode 12Y and the second electrode 12Z, to thereby use the recovered voltage as a driving voltage upon the next discharge.

[10] Referring to Fig. 2, energy recovering apparatus 30 and 32 of the PDP having been suggested by U.S. Patent No. 5,081,400 of Weber are symmetrically arranged with respect to each other with having a panel capacitor Cp there between. The panel capacitor Cp is an equivalent expression of a capacitance formed between the first electrode Y and the second electrode Y. The first energy recovering apparatus 30 applies a sustain pulse to the first electrode Y. The second energy recovering apparatus 32 operates alternately with respect to the first energy recovering apparatus 30 to thereby apply a sustain pulse to the second electrode Z. Fig. 3 is a timing diagram and a waveform diagram representing an on/off timing of switches in the first energy recovering apparatus and an output waveform of the panel capacitor. A detailed description can be found in this patent, and hence, a detailed explanation of the operation is omitted.

[11] Such conventional energy recovering apparatus 30 and 32 require many circuit elements resulting in increased manufacturing cost. Further, power consumption is relatively large due to a conduction loss of a plurality of switches, an inductor, etc., on the current path.

[12] Fig. 4 illustrates an energy recovering apparatus for a plasma display panel disclosed by U.S. Patent No. 5,670,974 which includes a panel capacitor 40 equivalently representing a capacitance formed between a scanning electrode and a sustain electrode of the plasma display panel 1, and a charging/discharging circuit 2 and a voltage clamp circuit 3 connected in parallel with the panel capacitor Cp. The charging/discharging circuit 2 includes a coil 8 connected in parallel with the panel capacitor 40 of the panel 1

to re-charge a reverse polarity of a resonant current generated when the panel capacitor 40 is discharged, and two switches 12 and 13.

[13] The switches 12 and 13 form a bi-directional switch with respect to the coil 8. One side of the panel capacitor 40 is connected, in series, to the two switches 12 and 13 formed from N-channel FET's controlled by different switch drive inputs IN5 and IN6 supplied to their respective gate terminals and reverse current blocking diodes 10 and 11 connected in series with the respective switches 12 and 13. Other side of the panel capacitor 40 is connected to one end of the parallel circuit having the coil 8 and a resistor 9. To the other end of the parallel circuit, the other terminal of the diodes 10 and 11 are connected commonly. The resistor 9 connected in parallel with the coil 8 of the charging/discharging circuit 2 is a damping resistor provided for the purpose of preventing an oscillation of a waveform.

[14] The voltage clamp circuit 3 includes first to fourth switches 4, 5, 6 and 7, of which the first and third switches 4 and 6 are respectively connected between one of two terminals of the panel capacitor 40 and power source terminals GND and -VS while the second and fourth switches 5 and 7 are respectively connected between the other of the terminals of the panel capacitor 40 and the power source terminals GND and -VS. The first and second switches 4 and 5 are P-channel FET's, and the third and fourth switches 6 and 7 are N-channel FET's. The switches 4, 6 and the switches 5, 7 form the CMOS type circuit structures, respectively.

[15] Fig. 5 is a waveform diagram representing drive voltage and drive current waveforms in the panel shown in Fig. 4. Figures 6A-6D illustrate the current path during time periods A-D of Figure 5. A detailed description can be found in this patent, and hence, a detailed explanation of the operation is omitted.

[16] The energy recovering apparatus suggested by U. S. Patent No. 5,679,094 of NEC corporation requires an energy recovering circuit and a sustain circuit for each of the scanning electrode and the sustain electrode of the plasma display panel 1 to thereby cause a complex circuit configuration. Accordingly, it has a problem in that a manufacturing cost rises. Furthermore, power consumption is relative large due to the conduction loss of the switches.

[17] The above references are incorporated by reference herein where appropriate for appropriate teachings of additional or alternative details, features and/or technical background.

### **SUMMARY OF THE INVENTION**

[18] An object of the invention is to solve at least the above problems and/or disadvantages and to provide at least the advantages described hereinafter.

[19] An object of the present invention is to provide an energy recovering apparatus and method thereof for a plasma display panel.

[20] An object of the present invention is to minimize a conduction loss.

[21] An object of the present invention is to simplify a circuit configuration.

[22] In order to achieve these and other objects of the invention, an energy recovering apparatus of a plasma display panel according to one aspect of the present invention includes a plasma display panel; a voltage source for supplying a sustain voltage, via a single of switching device, to the panel; an inductor for recovering an energy stored in the panel by a resonance provided by itself and the panel and applying the recovered energy to the panel; and a switching circuit for forming a current path between the inductor and the panel.

[23] In the energy recovering apparatus, the voltage source includes a first voltage source for charging the panel into a sustain voltage having a first polarity; and a second voltage source for charging the panel into a sustain voltage having a second polarity different from the first polarity.

[24] The switching device includes a first switch for forming a current path between the first voltage source and the panel; and a second switch for forming a current path between the second voltage source and the panel.

[25] Herein, the first switch forms a current path between the first voltage source and the panel in a period when a voltage of the panel is kept at the first polarity of sustain voltage.

[26] The second switch forms a current path between the second voltage source and the panel in a period when a voltage of the panel is kept at the second polarity of sustain voltage.

[27] The switching circuit includes third and fourth switches arranged, in parallel, between the inductor and the panel; a first diode connected between the third switch and a panel capacitor to shut off a backward current from the panel capacitor; and a second diode connected between the fourth switch and the panel capacitor to shut off a backward current from the fourth switch.

[28] The third switch forms a current path between the inductor and the panel going by way of the first diode in a period when a voltage of the panel rises from the second polarity of sustain voltage into the first polarity of sustain voltage.

[29] The fourth switch forms a current path between the inductor and the panel going by way of the second diode in a period when a voltage of the panel falls from the first polarity of sustain voltage into the second polarity of sustain voltage.

[30] An energy recovering apparatus of a plasma display panel according to another aspect of the present invention includes a plasma display panel; a voltage source for charging the panel; an inductor for charging an energy from the voltage source during a period when a voltage of the panel is kept at a sustain voltage by means of the voltage source; and switching devices for shutting off a path between the panel and the voltage source in a state in which an energy has been charged in the inductor to derive an inverse voltage into the inductor and supplying the inverse voltage to the panel.

[31] In the energy recovering apparatus, the inductor stores an energy recovered from the panel and supplies the derived inverse voltage to the panel.

[32] The voltage source includes a first voltage source for charging the panel into a sustain voltage having a first polarity; and a second voltage source for charging the panel into a sustain voltage having a second polarity different from the first polarity.

[33] The switching device includes a first switching device connected between the first voltage source and the panel; a second switching device connected between the second voltage source and the panel; third and fourth switching devices connected, in parallel, between the inductor and the panel; a first diode connected between the third switch and a panel capacitor to shut off a backward current from the panel capacitor; and a second diode connected between the third switch and the panel capacitor to shut off a backward current from the fourth switch.

[34] The first switching device forms a path between the first voltage source and the panel in a period when a voltage of the panel is kept at the first polarity of sustain voltage, and shuts off said path between the first voltage source and the panel when a voltage of the panel falls from the first polarity of sustain voltage.

[35] The second switching device forms a path between the second voltage source and the panel in a period when a voltage of the panel is kept at the second polarity of sustain voltage, and shuts off the path between the second voltage source and the panel when a voltage of the panel rises into the first polarity of sustain voltage.

[36] The third switching device forms a path between the inductor and the panel in a period when a voltage of the panel is charged into the first polarity of sustain voltage in a state kept at the second polarity of sustain voltage.

[37] The fourth switching device forms a path between the inductor and the panel in a period when a voltage of the panel is charged into the second polarity of sustain voltage in a state kept at the first polarity of sustain voltage.

[38] An energy recovering method for a plasma display panel according to still another aspect of the present invention includes (A) charging a voltage from a voltage source into the panel via a single of switching device; and (B) recovering an energy stored in the panel with the aid of a resonance provided by an inductor and the panel and applying the recovered energy to the panel.

[39] In the energy recovering method, step (A) includes charging a sustain voltage having a first polarity from a first voltage source into the panel; and charging a sustain voltage having a second polarity different from the first polarity supplied from a second voltage source into the panel.

[40] Step (A) further includes forming a current path between the first voltage source and the panel using the first switch; and forming a current path between the second voltage source and the panel using the second switch.

[41] The first switch forms a current path between the first voltage source and the panel in a period when a voltage of the panel is kept at the first polarity of sustain voltage.

[42] The second switch forms a current path between the second voltage source and the panel in a period when a voltage of the panel is kept at the second polarity of sustain voltage.

[43] Step (B) includes forming a current path between the inductor and the panel using the third switch; shutting off a backward current from the panel using a first diode connected between the third switch and the panel; forming a current path between the inductor and the panel using the fourth switch connected, in parallel, to the third switch; and shutting off a backward current from the fourth switch using a second diode connected between the fourth switch and the panel.

[44] The third switch forms a current path between the inductor and the panel going by way of the first diode in a period when a voltage of the panel rises from the second polarity of sustain voltage into the first polarity of sustain voltage.

[45] The fourth switch forms a current path between the inductor and the panel going by way of the second diode in a period when a voltage of the panel falls from the first polarity of sustain voltage into the second polarity of sustain voltage.

[46] An energy recovering method for a plasma display panel according to still another aspect of the present invention includes (A) charging a voltage from a voltage source into the panel; (B) charging an energy from the voltage source into an inductor during a period when a voltage of the panel is kept at a sustain voltage by means of the voltage source; and (C) shutting off a path between the panel and the voltage source in a state in which an energy has been charged in the inductor using switching devices to

derive an inverse voltage into the inductor and supplying said inverse voltage to the panel.

[47] In the energy recovering method, the inductor stores an energy recovered from the panel and applies the derived inverse voltage to the panel.

[48] Step (A) includes charging a sustain voltage having a first polarity from a first voltage source into the panel; and charging a sustain voltage having a second polarity different from the first polarity supplied from a second voltage source into the panel.

[49] Step (A) further includes forming a current path between the first voltage source and the panel in a period when a voltage of the panel is kept at the first polarity of sustain voltage; and shutting off the path between the first voltage source and the panel when a voltage of the panel falls from the first polarity of sustain voltage.

[50] Step (A) further includes forming a current path between the second voltage source and the panel in a period when a voltage of the panel is kept at the second polarity of sustain voltage; and shutting off the path between the second voltage source and the panel when a voltage of the panel rises into the first polarity of sustain voltage.

[51] The energy recovering method further includes the step of forming a path between the inductor and the panel in a period when a voltage of the panel is charged into the first polarity of sustain voltage in a state kept at the second polarity of sustain voltage.

[52] The energy recovering method further includes the step of forming a path between the inductor and the panel in a period when a voltage of the panel is charged into the second polarity of sustain voltage in a state kept at the first polarity of sustain voltage.

[53] Additional advantages, objects, and features of the invention will be set forth in part in the description which follows and in part will become apparent to those having ordinary skill in the art upon examination of the following or may be learned from practice of the invention. The objects and advantages of the invention may be realized and attained as particularly pointed out in the appended claims.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

[54] The invention will be described in detail with reference to the following drawings in which like reference numerals refer to like elements wherein:

[55] Fig. 1 is a perspective view representing a preferable structure of a plasma display panel;

[56] Fig. 2 is a circuit diagram of a conventional energy recovering apparatus;

[57] Fig. 3 is a timing diagram and a waveform diagram representing an ON/OFF timing of each switch shown in Fig. 2 and an output waveform of the panel capacitor;

[58] Fig. 4 is a circuit diagram of another conventional energy recovering apparatus;

[59] Fig. 5 is a timing diagram and a waveform diagram representing an ON/OFF timing of each switch shown in Fig. 4 and an output waveform of the panel capacitor;

[60] Fig. 6A is a circuit diagram representing an ON/OFF state and a current path of the switching device in the A' period shown in Fig. 5;

[61] Fig. 6B is a circuit diagram representing an ON/OFF state and a current path of the switching device in the B period shown in Fig. 5;

[62] Fig. 6C is a circuit diagram representing an ON/OFF state and a current path of the switching device in the C period shown in Fig. 5;

[63] Fig. 6D is a circuit diagram representing an ON/OFF state and a current path of the switching device in the D period shown in Fig. 5;

[64] Fig. 6E is a circuit diagram representing an ON/OFF state and a current path of the switching device in the A period shown in Fig. 5;

[65] Fig. 7 is a circuit diagram of an energy recovering apparatus of a plasma display panel according to one of the embodiments of the present invention;

[66] Fig. 8 is a timing diagram and a waveform diagram representing an ON/OFF timing of each switch and an output waveform of the panel capacitor in the energy recovering apparatus of the plasma display panel according to a first embodiment shown in Fig. 7;

[67] Fig. 9 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the T0 period shown in Fig. 8;

[68] Fig. 10 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the T1 period shown in Fig. 8;

[69] Fig. 11 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the T2 period shown in Fig. 8;

[70] Fig. 12 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the T3 period shown in Fig. 8;

[71] Fig. 13 is a timing diagram and a waveform diagram representing an ON/OFF timing of each switch and an output waveform of the panel capacitor in the energy recovering apparatus of the plasma display panel according to one of the embodiments shown in Fig. 7;

[72] Fig. 14 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the TA interval of the T0 period shown in Fig. 13;

[73] Fig. 15 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the TB interval of the T0 period shown in Fig. 13;

[74] Fig. 16 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the T1 period shown in Fig. 13;

[75] Fig. 17 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the TC interval of the T2 period shown in Fig. 13;

[76] Fig. 18 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the TD interval of the T2 period shown in Fig. 13; and

[77] Fig. 19 is a circuit diagram representing an ON/OFF state and a current path of the switching device in the T3 period shown in Fig. 13.

#### **DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS**

[78] Referring to Fig. 7, there is shown an energy recovering apparatus of a plasma display panel (PDP) according to one of the first embodiments of the present invention.

A panel capacitor  $C_p$  is equivalently formed at first and second electrodes of the PDP. A first sustain voltage source  $+VS$  provides a first polarity of voltage  $+VS$ , and a second sustain voltage source  $-VS$  provides a second polarity of voltage  $-VS$ . A first switch  $Q1$  is connected between the first sustain voltage source  $+VS$  and a first electrode of the panel capacitor  $C_p$ , and a second switch  $Q2$  is connected between the second sustain voltage source  $-VS$  and the first electrode. An inductor  $L$  is connected between a first node  $N1$  positioned between the first and second switches  $Q1$  and  $Q2$  and a second node  $N2$

positioned between the first and second voltage source +VS and -VS. Third and fourth switches Q3 and Q4 is connected, in parallel, between the inductor L and the first node N1.

[79] The first sustain voltage source +VS generates a positive sustain voltage +VS supplied to the panel capacitor Cp. The second sustain voltage source -VS generates a negative sustain voltage -VS supplied to the panel capacitor Cp.

[80] Each of the first and second switches Q1 and Q2 is connected, in parallel, to one end of the panel capacitor Cp, that is, the first node (i.e., the first electrode). Each of the third and fourth switches Q3 and Q4 is connected, in parallel, between the inductor L and the first node N1 in a mutually different direction. The inductor L is connected, via the third and fourth switches Q3 and Q4, to the panel capacitor Cp to recover an energy by an LC natural resonance provided from itself and the panel capacitor Cp, and again applies the recovered energy to the panel capacitor Cp.

[81] Preferably, first to fourth switches Q1 to Q4 are sequentially turned on to thereby control a flow of current. A diode is connected, in parallel, to each of the first to fourth switches Q1 to Q4. The diodes can be used as internal diodes of the first to fourth switches Q1 to Q4. Alternatively, the diodes may be used as external diodes thereof. Each of the first to fourth switches Q1 to Q4 employs any one of semiconductor switching devices such as a metal oxide semiconductor field-effect transistor (MOSFET), an insulated gate bipolar transistor (IGBT), a silicon-controlled rectifier (SCR), a bipolar junction transistor (BJT) and a high electron mobility transistor (HEMT), etc. Further, other switches and/or configurations are possible, which are illustrated in co-pending Application Serial Nos. 10/416,286 filed May 9, 2003, (OP3095) filed \_\_\_\_\_, and

(OP3096) filed \_\_\_\_\_, assigned to the same Assignee, whose entire disclosure is incorporated herein by reference.

[82] Meanwhile, a first diode D1 for shutting off a backward current from the panel capacitor Cp is connected between the third switch S3 and the first node N1 while a second diode D2 for shutting off a backward current from the fourth switch Q4.

[83] Fig. 8 is a timing diagram and a waveform diagram representing an ON/OFF timing of each switch shown in Fig. 7 and a voltage applied to the panel capacitor.

An energy recovering apparatus and method according to one of the embodiments will be described in conjunction with Figures 7-12.

[84] First, in a T0 period, the first switch Q1 of the first to fourth switches Q1 to Q4 is preferably turned on, to form a current path extending from the first sustain voltage source +VS, via the first switch Q1, the first node N1, the panel capacitor Cp and the second node N2, into the first sustain voltage +VS, as shown in Fig. 9. Accordingly, the panel capacitor Cp charges a sustain voltage +VS supplied from the first sustain voltage source +VS. Thus, the panel capacitor Cp keeps a positive sustain voltage +VS.

[85] In a T1 period, the first switch Q1 is turned off while the fourth switch Q4 is preferably turned on, to form a current path extending from the panel capacitor Cp, via the first node N1, the second diode D2, the fourth switch Q4 and the inductor L, into the panel capacitor Cp, as shown in Fig. 10. Accordingly, the inductor L recovers an energy charged in the panel capacitor Cp with the aid of an LC natural resonance provided by itself and the panel capacitor Cp. Thus, a voltage of the panel capacitor Cp falls from the positive sustain voltage +VS into the negative sustain voltage -VS.

[86] In a T2 period, the fourth switch Q4 is turned off while the second switch Q2 only is turned on, to thereby form a current path extending from the second sustain voltage source -VS, via the second node N2, the panel capacitor Cp, the first node N1 and the second switch Q2, into the second sustain voltage source -VS, as shown in Fig. 11. Accordingly, the panel capacitor Cp receives a negative sustain voltage -VS from the second sustain voltage -VS to keep the negative sustain voltage -VS.

[87] In a T3 period, the second switch Q2 is turned off while the third switch Q3 is preferably turned on, to thereby form a current path extending from the panel capacitor Cp, via the second node N2, the inductor L, the third switch S3, the first diode D1 and the first node N1, into the panel capacitor Cp as shown in Fig. 12. Accordingly, the inductor L recovers an energy charged in the panel capacitor Cp with the aid of an LC natural resonance provided by itself and the panel capacitor Cp, and again applies the recovered energy to the panel capacitor Cp. Thus, a voltage of the panel capacitor Cp rises from the negative sustain voltage -VS into the positive sustain voltage +VS.

[88] Such T0 to T3 periods are periodically repeated to thereby apply an alternating current sustain pulse Vcp to the panel capacitor Cp. The AC driving pulse Vcp applied to the first and second electrodes Y and Z of the PDP is generated with periodically repeating the above-mentioned T0 to T3 periods.

[89] As described above, such an energy recovering apparatus and method of the PDP according to the first embodiment of the present invention uses an LC natural resonance provided by the inductor L and the panel capacitor Cp to recover an energy charged in the panel capacitor Cp, and again applies the recovered energy to the panel capacitor. Thus, the energy recovering apparatus and method of the PDP according to this embodiment can preferably use one inductor and one switching device between the

first electrode Y and the second electrode Z as the energy-recovering current path of the panel capacitor. Hence, a conduction loss and a switching loss of the semiconductor device is minimized.

[90] Fig. 13 is a timing diagram and a waveform diagram representing an ON/OFF timing of each switch shown in Fig. 7 and a voltage applied to the panel capacitor.

[91] An energy recovering apparatus and method according to the another embodiment will be described in conjunction with Figures 7, 13 and 14-19. A T0 period is divided into a TA interval when the first switch Q1 is turned on, and a TB interval when both the first and fourth switches Q1 and Q4 are turned on. In the TA interval of the T0 period, the first switch Q1 of the first to fourth switches Q1 to Q4 is preferably turned on, to form a current path extending from the first sustain voltage source +VS, via the first switch Q1, the first node N1, the panel capacitor Cp and the second node N2, into the first sustain voltage +VS, as shown in Fig. 14. Accordingly, the panel capacitor Cp charges a sustain voltage +VS supplied from the first sustain voltage source +VS.

[92] In the TB interval of the T0 period, the fourth switch Q4 is turned on in a state in which the first switch Q1 has been turned on, to form a current path extending from the first sustain voltage source +VS, via the first switch Q1, the first node N1, the second diode D2, the fourth switch Q4, the inductor L and the second node N2, into the first sustain voltage +VS. Accordingly, the sustain voltage +VS charged in the panel capacitor Cp is kept, as shown in Figure 14, and the inductor L charges a current IL from the first sustain voltage source +VS, as shown in Figure 15.

[93] In a T1 period, the fourth switch Q4 keeps a turned-on state while the first switch Q1 is turned off, to form a current path extending from the inductor L, via the panel capacitor Cp, the first node N1, the second node D2 and the fourth switch Q4, into the inductor L, as shown in Fig. 16. Thus, the inductor L recovers and supplies a current charged in the panel capacitor Cp with the aid of an inverse voltage generated by a reverse electromotive force when the first switch Q1 is turned off. Accordingly, the panel capacitor Cp falls into a negative sustain voltage -VS by an inverse voltage supplied from the inductor L. The inductor L recovers and supplies a current charged in the panel capacitor Cp using the inverse voltage in this manner, thereby obtain a rapid falling slope of the sustain voltage waveform.

[94] A T2 period is divided into a TC interval when only the second switch Q2 only is turned on and a TD interval when both the second and third switches Q2 and Q3 are turned on. In the TC interval of the T2 period, the second switch Q2 of the first to fourth switches Q1 to Q4 is turned on, to thereby form a current path extending from the second sustain voltage source -VS, via the second node N2, the panel capacitor Cp, the first node N1 and the second switch Q2, into the second sustain voltage -VS, as shown in Fig. 17. Thus, the panel capacitor Cp receives a negative sustain voltage -VS from the second sustain voltage source -VS to keep the negative sustain voltage -VS of the T1 period.

[95] In the TD interval of the T2 period, the third switch Q3 is turned on in a state in which the second switch Q2 has been turned on, to thereby form a current path extending from the second sustain voltage source -VS, via the second node N2, the inductor L, the third switch Q3, the first diode D1, the first node N1 and the second node Q2, into the second sustain voltage source -VS. Thus, the negative sustain voltage

–VS charged in the panel capacitor Cp is kept, as shown in Figure 17, and the inductor L charges a current IL from the second sustain voltage source –VS, as shown in Figure 18.

[96] In a T3 period, the third switch Q3 is turned on while the second switch Q2 is turned off, to form a current path extending from the inductor L, via the third switch Q3, the first diode D1, the first node N1, the panel capacitor Cp and the second node N2, into the inductor L, as shown in Fig. 19. Thus, the inductor L recovers and supply a current charged in the panel capacitor Cp using an inverse voltage generated by a reverse electromotive force when the second switch Q2 is turned off. Accordingly, the panel capacitor Cp rises into the sustain voltage +VS by the inverse voltage supplied from the inductor L. The current charged in the panel capacitor Cp is recovered and supplied with the aid of the inverse voltage derived from the inductor L, thereby obtaining a rapid rising slope of the sustain voltage waveform.

[97] Such T0 to T3 periods are periodically repeated to thereby apply an alternating current sustain pulse to the panel capacitor Cp. The AC driving pulse Vcp applied to the first and second electrodes Y and Z of the PDP is generated with periodically repeating the above-mentioned T0 to T3 periods.

[98] Such an energy recovering apparatus and method of the PDP according to the embodiments of the present invention charges an energy into the inductor L in the course of supplying the sustain voltage +VS to the panel capacitor Cp, and recovers and supplies an energy charged in the panel capacitor Cp using an inverse voltage upon recovering of the energy. Accordingly, it is possible to obtain rapid rising and falling slopes of the sustain waveform upon recovering of the energy.

[99] As described above, the energy recovering apparatus and method of the PDP according to the embodiments of the present invention charges an energy into the

inductor in the course of supplying the sustain voltage +VS to the panel capacitor, and recovers and supplies an energy charged in the panel capacitor using an inverse voltage upon recovering of the energy. Accordingly, it is possible to obtain rapid rising and falling slopes of the sustain waveform upon recovering of the energy.

[100] Furthermore, the energy recovering apparatus and method of the PDP according to the embodiments of the present invention has an advantage in that it can be configured at any one side of the first and second electrodes of the PDP. Also, a switching device exists in the sustain current path, so that it is possible to minimize a conduction loss. In addition, the energy recovering apparatus of the PDP according to the embodiments of the present invention uses four switching devices and two diodes, thereby reducing power consumption.

[101] The foregoing embodiments and advantages are merely exemplary and are not to be construed as limiting the present invention. The present teaching can be readily applied to other types of apparatuses. The description of the present invention is intended to be illustrative, and not to limit the scope of the claims. Many alternatives, modifications, and variations will be apparent to those skilled in the art. In the claims, means-plus-function clauses are intended to cover the structures described herein as performing the recited function and not only structural equivalents but also equivalent structures.